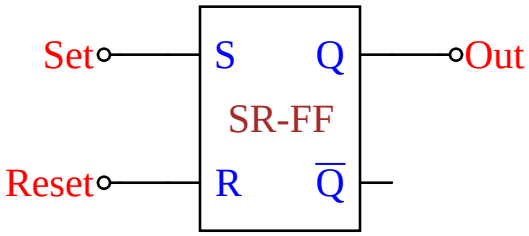
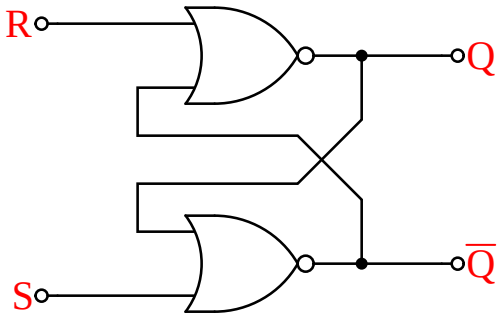
S-R Flip-flop instance

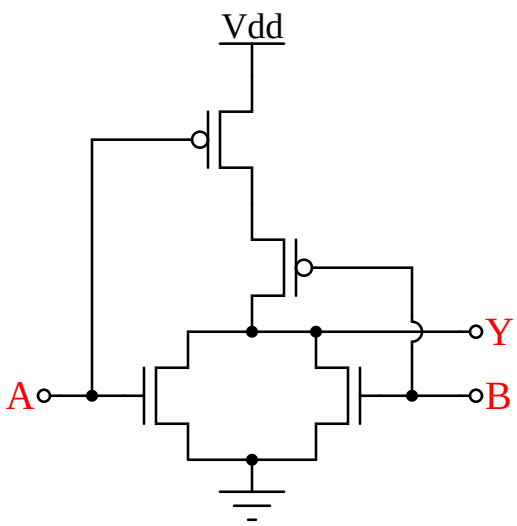


S-R Flip-flop Circuit

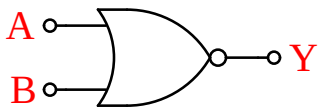


2-Input Nor gate

schematic



symbol



logic table

A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0